Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7472	709/213-216.ccls. 709/229.ccls. 709/250.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 10:57
L2	1962	710/15-19.ccls. 709/33.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 10:57
L4	11876	(memory cache ram) near10 (insufficient sufficient available enough) near5 (room space)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 12:0:
L5	513786	(adapter\$1 adaptor\$1 nic ("i/o" input comunication\$1 interface) adj (apparatus card\$1 device\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 10:5
L7	288955	(mov\$3 transfer\$4) near10 (queue\$1 instruction\$1 command\$1 block\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF -	2007/09/22 11:5
L8	34892	(memory ram cache) near10 full	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 10:5
L9	8813	5 same 7	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L10	576	5 same 8	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L11	268	1 and 9	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L12	94	2 and 9	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L13	23	1 and 10	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L14	5	2 and 10	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:1
L16	327	5 same 4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L17	169	16 and 7	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L19	3	2 and 16	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:1
L20	135745	(memory cache ram) near10 (insufficient sufficient available enough)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L22	3500	7 same 20	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:1
L23	129	5 same 7 same 20	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0
L24	10	2 and 7 same 20	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:0

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L25	319	2 and 20	US-PGPUB; USPAT; EPO; JPO	OR -	OFF	2007/09/22 11:13
L26	. 88	2 and 8	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:13
L28	892	22 same (computer host system) near5 (memory ram cache)	US-PGPUB; USPAT; EPO; JPO	·OR	OFF	2007/09/22 11:17
L29	192	"710"/\$.ccls. and 28	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:19
L30	68	"709"/\$.ccls. and 28	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:18
L42	2409	710/52.ccls. 710/56.ccls.	US-PGPUB; USPAT; EPO; JPO	OR .	OFF	2007/09/22 11:28
L45	99	42 and 20 same 7	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:38
L46	43	42 and 8 same 7	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:38
L47	2622	370/412.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:38
L48	45	47 and 20 same 7	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:39
L49	16	47 and 8 same 7	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:38
L50	2501	(host system computer) near3 (memory ram cache) same (adapter adaptor nic (input "i/o" interface communication) near2 (device\$1 card\$1 apparatus)) same (send\$3 transmit\$4 mov\$3 transfer\$4) near10 (queue\$1 instruction\$1 command\$1 block\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:54
L51	927	50 and 20	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 12:02
L52	33,5	50 and 8	US-PGPUB; USPAT; EPO; JPO	OR .	OFF	2007/09/22 12:02
L56	. 79	(51 52) and 1	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 11:55
L57	1819	(determin\$3 check\$3) near10 (memory cache ram) near10 (insufficient sufficient available enough) near5 (free room space)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 12:01
L58	97	50 same 20	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 12:03
L59	18	50 same 8	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/22 12:03

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S1	773	(adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1)) same (memory ram cache) near10 (receiv\$4 load\$3) near10 (instruction\$1 (transmission communication\$1) adj (control\$1 protocol\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:49
S2	1833346	host comput\$3 server	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:11
S3	527	S1 same S2	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:21
S4	34599	(memory ram cache) near10 full	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:11
S5	85	S1 same S2 and S4	US-PGPUB; USPAT; EPO; JPO	OR F	OFF	2007/08/21 14:25
S6	832	(adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1)) same (memory ram cache) near10 (receiv\$4 load\$3) near10 (instruction\$1 command\$1 information data) near3 (transmission communication\$1 protocol translation\$1 conversion\$1 control)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:25
S 7	3	S6 same S4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:21
5 8	38	S6 same S2 and S4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:4
S9	134442	(memory cache ram) near10 (insufficient sufficient available enough)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:0
S10	195410	(mov\$3 transfer\$4) near10 (control transmission communication\$1 conversion\$1 translation\$1) near3 (data information protocol\$1 command\$1 instruction\$1 software\$1 program\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:19
S11	297988	(adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:5
S12	5856	S10 same S11	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:5
S13	101	S9 same S10 same S11	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 15:4
S14	30046	(memory cache ram) near10 (exceed\$3 meet\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 14:5
S15	21	S14 same S10 same S11	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:2
S16	392999	(send\$3 transmit\$4) near10 (control transmission communication\$1 conversion\$1 translation\$1) near3 (data information protocol\$1 command\$1 instruction\$1 software\$1 program\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:3
S17	38	S9 same S16 same S11	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:2
S18	7	S14 same S16 same S11	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:2

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S19	6980	(memory cache ram) near10 (out "no") near5 (room space)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:33
S20	1355555	(send\$3 transmit\$4 mov\$3 transfer\$4 remov\$3) near10 (data information protocol\$1 command\$1 instruction\$1 software\$1 program\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:37
S21	26	S11 same S19 same S20	US-PGPUB; USPAT; EPO; JPO	OR .	OFF	2007/08/21 16:41
S22	81	S11 same S4 same S20	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 16:41
S23	11734	(memory cache ram) near10 (insufficient sufficient available enough) near5 (room space)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:36
S24	2011	S20 same S23	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:21
S25	41	S11 same S24	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:17
S26	92381	(host comput\$3) same (adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:22
S27	6048	S20 same S4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:21
S28	341	S24 and S26	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:21
S29	. 724	S27 and S26	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:21
S 30	83	"710"/\$.ccls. and S28	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:23
S31	164	"710"/\$.ccls. and S29	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:23
S32	131	"709"/\$.ccls. and S29	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:23
S33	70	"709"/\$.ccls. and S28	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:23
S34	4263	(adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1)) near10 (memory ram cach\$3 stor\$3) near10 (transmission communication\$1 protocol translation\$1 conversion\$1 control) near3 (instruction\$1 command\$1 information data)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 10:51
S35	10	S34 same S4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 13:00
S36	4	S34 same S23	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:30
S37	88	S34 and (adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1)) same S4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 13:05

538	63	S34 and (adapter\$1 adaptor\$1 nic (comunication\$1 interface)	US-PGPUB;	OR	·OFF	2007/08/21 17:30
		adj (card\$1 device\$1)) same S23	USPAT; EPO; JPO			
S39	3441	(adapter\$1 adaptor\$1 nic (comunication\$1 interface) adj (card\$1 device\$1)) near10 (cach\$3 stor\$3) near10 (transmission communication\$1 protocol translation\$1 conversion\$1 control) near3 (protocol\$1 instruction\$1 command\$1 information data)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:43
S41	. 6	S4 same S39	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:45
S 42	134442	(memory cache ram) near10 (insufficient sufficient available enough)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:4
S43	62	S42 same S39	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/08/21 17:4
S47	329669	(adapter\$1 adaptor\$1 nic (comunication\$1 network interface) adj (card\$1 device\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 10:5
S52	29832	memory near10 ("no" out) same (mov\$3 transfer\$4) near10 (data information)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 11:3
S53	654	S52 same S47	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 11:3
S54	388	memory near5 (room space) near5 ("no" out) same (mov\$3 transfer\$4) near10 (data information)	US-PGPUB; USPAT; EPO; JPO	OR .	OFF	2007/09/07 11:3
S56	4244	(cache ram memory) near10 (unavailable full insufficient) same (mov\$3 transfer\$4) near10 (data information)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 11:4
S57	. 77	S56 same S47	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 11:3
S58	687	(cache ram memory) near10 (unavailable full insufficient) same (transmit\$4 send\$3 mov\$3 transfer\$4) near10 (host system computer) near3 memory	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 11:4
S59	23	S58 same S47	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 12:0
S66	.1177	(cache ram memory) near7 "no" same (transmit\$4 send\$3 mov\$3 transfer\$4) near10 (host system computer) near3 memory	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 11:5
S67	44	S66 same S47	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/07 12:0
S68	1956	710/15-19.ccls. 709/33.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 13:0
S 69	34790	(memory cache ram) near10 full	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:0
S 70	88	S68 and S69	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 13:0
S71	1758383	(transmit\$4 transfer\$4 mov\$3 send\$3) near10 (data information queue\$1 control transmission tcp)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:0

S 73	511773	(adapter\$1 adaptor\$1 nic ("i/o" input comunication\$1 interface) adj (apparatus card\$1 device\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 16:36
S74	58	"710"/\$.ccls. and S69 same S71 same S73	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 13:55
S75	8830	(memory cache ram) near10 ("no" near3 (room space) insufficient)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 13:09
S78	163	S69 same S71 same S73	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:02
S80	37	S75 same S71 same S73	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:02
S81	43512	(memory cache ram) near10 ("no" near3 (room space) full unavailable insufficient)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:11
S82	4414085 ·	(transfer\$4 mov\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:08
S83	· 203	S81 same S82 same S73	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:10
S 85	6952	cache same (memory cache ram) near10 ("no" near3 (room space) full unavailable insufficient)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:11
S86	125	S73 same S85	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:12
S87	60	S86 not S83 not S78	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/11 14:12
S90	34790	(memory ram cache) near10 full	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:43
S92	11830	(memory cache ram) near10 (insufficient sufficient available enough) near5 (room space)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 11:27
S93	43763	S90 S92	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:37
S94	26185	(transmit\$4 send\$3 mov\$3 transfer\$4) near10 queue	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:42
S 95	309	S93 same S94	US-PGPUB; USPAT; EPO; JPO	OR .	OFF	2007/09/12 10:39
S96	511773	(adapter\$1 adaptor\$1 nic ("i/o" input comunication\$1 interface) adj (apparatus card\$1 device\$1))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:37
S 97	11	S93 same S94 same S96	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:39
S10 1	288159	(mov\$3 transfer\$4) near10 (queue\$1 instruction\$1 command\$1 block\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:44

S10 2	314896	(system host) near5 memory	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:44
\$10 3	1021	S101 same S102 same S96	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:44
S10 4	559	S101 same S102 same S96 near5 (ram memory cache)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:57
S10 7	7	S104 same S93	US-PGPUB; USPAT; EPO; JPO	OR .	OFF	2007/09/12 10:45
S10 8	243	S93 near10 S96	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 10:57
S11 0	10293	determin\$3 near10 (memory cache ram) near10 (full insufficient sufficient available enough)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:47
S11 2	6	S94 same S96 same S110	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:48
S11 3	151	S96 same S110	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:30
S11 4	131	S113 not S108	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:31
S11 5	34	"710"/\$.ccls. and S113	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:31
S11 6	4896	determin\$3 near10 (room space) near10 (memory cache ram)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:49
S11 7	2	S94 same S96 same S116	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:48
S11 8	117	S96 same S116	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:49
S11 9	6	S96 same S116 same S101	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:49
S12 0	88019	(room space) near10 (memory cache ram)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:49
S12 1	157	S96 same S120 same S101	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2007/09/12 13:49